

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

DAE WOO LEE, ET AL.

Application No.:

Filed:

For: **Structure of High Voltage Device and Low
Voltage Device, and Method of
Manufacturing the Same**

Art Group:

Examiner:

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

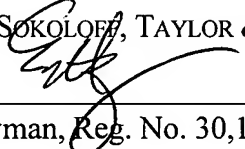
In accordance with the duty of disclosure, enclosed is a copy of Information Disclosure Statement by Applicant (form PTO/SB/08), which is being submitted . It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Date: November 24 2003

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP


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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Application Number	
Filing Date	
First Named Inventor	Dae Woo Lee
Art Unit	
Examiner Name	
Attorney Docket Number	5882P063

[illegible]

Date	
Considered	

Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 08/11/2003.
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